

IN THE UNITED STATES PATENT OFFICE

Applicant: Warren E. Cory  
Assignee: Xilinx, Inc.  
Title: Variable Latency Buffer and Method of Operation  
Serial No.: 10/660,449 File Date: 9/11/2003  
Examiner: Tanh Q. Nguyen Art Unit: 2182  
Docket No.: X-1214 US Conf. No.: 4907

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AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

In response to the Final Office Action mailed from the Patent Office on September 3, 2008, please replace/substitute the following claim(s), as indicated.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 5 of this paper.

DO NOT ENTER: /T.N./ (10/30/2008)